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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/038,905	12/31/2001	Sushma Shrikant Trivedi	4860.P2694	3342
7590 04/04/2005			EXAMINER	
James C. Scheller			TUNG, KEE M	
BLAKELY, SO	KOLOFF, TAYLOR & 2	ZAFMAN LLP		
Seventh Floor			ART UNIT	PAPER NUMBER
12400 Wilshire Boulevard			2676	
Los Angeles, C	A 90025-1026			

DATE MAILED: 04/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
Office Assistant Comments	10/038,905	TRIVEDI ET AL.
Office Action Summary	Examiner	Art Unit
	Kee M Tung	2676
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed  s will be considered timely. the mailing date of this communication.
Status		
1) Responsive to communication(s) filed on 31 De	ecember 2001.	
2a) ☐ This action is <b>FINAL</b> . 2b) ☒ This	action is non-final.	
3) Since this application is in condition for allowar	nce except for formal matters, pro	secution as to the merits is
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 45	3 O.G. 213.
Disposition of Claims		
4) Claim(s) <u>1-78</u> is/are pending in the application.		
4a) Of the above claim(s) is/are withdrav	vn from consideration.	
5) Claim(s) is/are allowed.		•
6)⊠ Claim(s) <u>1,9,16,18,25,31,33,34,36,43,49,51,52</u>	<u>,54,61,67,69,70 and 72</u> is/are rej	ected.
7) Claim(s) <u>2-8,10-15,17,19-24,26-30,32,35,37-42</u>		and 73-78 is/are objected to.
8) Claim(s) are subject to restriction and/or	r election requirement.	
Application Papers		
9) The specification is objected to by the Examine	r.	
10)☐ The drawing(s) filed on is/are: a)☐ acce	epted or b) objected to by the E	Examiner.
Applicant may not request that any objection to the o		
Replacement drawing sheet(s) including the correcti		
11) The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.
Priority under 35 U.S.C. § 119		
<ul> <li>12) Acknowledgment is made of a claim for foreign</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents</li> </ul>		-(d) or (f).
2. Certified copies of the priority documents		on No.
3. Copies of the certified copies of the prior		
application from the International Bureau		
* See the attached detailed Office action for a list of	of the certified copies not receive	d.
Attachment(s)		
Notice of References Cited (PTO-892)	4) Interview Summary (	(PTO-413)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	Paper No(s)/Mail Da	te
Paper No(s)/Mail Date <u>12/31/01</u> .	5)  Notice of Informal Pa	atent Application (PTO-152)
	,	

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## **DETAILED ACTION**

## **Double Patenting**

1. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970);and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

2. Claims 1, 9, 16, 18, 25, 31, 33, 34, 36, 43, 49, 51, 52, 54, 61, 67, 69, 70 and 72 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 33, 37, 43, 46, 48, 50, 52, 55, 58, 61, 63, 65, 67, 70, 73 and 76 of U.S. Patent No. 6,697,076. Although the conflicting claims are not identical, they are not patentably distinct from each other because the claims of present application would have been obvious in view of the patent claims because the claims in the present application is broad than the claims in the paten, for example.

P. N. 6,697, 076 (claim 33)	S. N. 10/038,905 (claim 25)	
A method, in a processor having a	A method in a computer system having at least	
cache coherent interface and a	one host processor with host processor cache	
cache non-coherent interface, the	and host memory coupled to a chipset having	
method comprising:	a cache coherent interface and a cache non-	

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	coherent interface, the method comprising:
receiving a memory access request	receiving a memory access request from a
for translation of a logical address to a	computer engine;
physical address;	
translating the logical address to the	
physical address through a memory	
mapping mechanism;	
determining whether the physical	examining the memory access request to
address is configured for cache	determine whether the memory access
coherent memory access;	request is target for cache coherent access;
wherein if the physical address is	wherein if the memory access request is
configured for cache coherent	targeted for cache coherent access,
memory access, transmitting the	transmitting the memory access request to
memory access request to the	the cache coherent interface; and
cache coherent interface; and	·
wherein if the physical address is	wherein if the memory access request is
configured for cache non-coherent	targeted for cache non-coherent access,
memory access, transmitting the	transmitting the memory access request to
memory access request to the	the cache non-coherent interface.
cache non-coherent interface.	

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## Conclusion

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kee M Tung whose telephone number is 571-272-7794. The examiner can normally be reached on Tuesday - Friday from 5:30 am - 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Bella can be reached on 571-272-7778. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kee M Tung
Primary Examiner

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